

VPEC PUBLICATIONS LIST 1980-1997

Year	Ref. #	Paper
1997/98		
	56	"New Charge Pump Power Factor Correction Electronic Ballast with a Wide Range of Line Input Voltage," Jinrong Qian and Fred C. Lee, APEC '98, VPEC '98 and IEEE Transactions on Power Electronics, Volume 14, Issue 1, January 1999.
	55	"Measurement of Noise Source Impedance of Off-Line Converters," Dongbing Zhang, Dan Y. Chen, Mark J. Nave, and Dan Sable, APEC '98, VPEC '98, and IEEE Transactions on Power Electronics, pg. 820-826, September 2000.
	54	"IEEE and International Harmonic Standards Impact on Power Electronic Equipment Design," Thomas S. Key and Jih-Sheng Lai, IECON '97 (11/97) and VPEC '98.
	53	"Fundamentals of a New Family of Auxiliary Resonant Snubber Inverters," Jih-Sheng Lai, IECON '97, November 1997 and VPEC '98.
	52	"Stability of Large DC Power Systems Using Switching Converters, with Application to the International Space Station," E. W. Gholdston, K. Karimi, F.C. Lee, J. Rajagopalan, Y. Panov, and B. Manners, IECEC '96.
	51	"Design of an ASIC Control Chip for a Forward Active-Clamp Converter," W. Dong and A. Huang, VPEC Seminar, September 1997.
	50	"Power Flow Analysis of Electrostrictive Actuators Driven by a PWM Amplifier," G. Zvonar, D. Lindner, and F.C. Lee, VPEC Seminar, September 1997.
	49	"Cascaded Three-Phase Buck PWM Rectifiers and their Soft-Switching," K. Wang, F.C. Lee, and D. Boroyevich, VPEC Seminar, September 1997.
	48	"Digital Control of Three-Phase Isolated ZVZCS PWM Buck Rectifier," S. Chandrasekaran, S. Dubovsky, K. Wang, and D. Boroyevich, VPEC Seminar, September 1997.
	47	"Charge Control of Three-Phase Buck PWM Rectifiers," K. Wang, D. Boroyevich, and F.C. Lee, VPEC Seminar, September 1997.
	46	"Multiple Levels of Complexity in Modeling and Simulation Power Supply Subsystems," Q. Li, J. Waite, R. Hodkiewicz, T. Wilson, Jr., and F.C. Lee, VPEC Seminar, September 1997.
	45	"Modeling, Simulation and Control of the Five Phase Brushless DC Motor Drive," A. Uan-Zo-li, K. Wang, S. Mazumder, F.C. Lee, and D. Boroyevich, VPEC Seminar, September 1997.
	44	"Investigation of Soft-Switching Techniques for Power Electronics Building Blocks (PEBB)," H. Dai, K. Xing, and F.C. Lee, VPEC Seminar, September 1997, and APEC '98.
	43	"Modeling, Control and Stability Analysis of a PEBB-Based DC DPS," G.S. Thandi, K. Xing, F.C. Lee, D. Boroyevich, VPEC Seminar, September 1997 and APEC '98
	42	"High Density Power Electronic Packaging," A. Ward and G.Q. Lu, VPEC Seminar, September 1997.
	41	"Neutral Voltage Shift and Grounding Issues in a DC Distributed System," K. Xing, F.C. Lee, J.S. Lai, T. Gurjit, and D. Boroyevich, VPEC Seminar, September 1997 and IAS '97.
	40	"Improved Soft-Switching ZVT Converters Using Coupled Inductor Based Active Snubber Cell," R. L. Lin, Y. Zhao, and F.C. Lee, VPEC Seminar, September 1997.
	39	"An All-Fiber-Optic Interface for Microprocessor Central of Three-Phase Power Converters," H. Zhu, H. Dai, J. Lai, F.C. Lee, and D. Boroyevich, VPEC Seminar, September 1997.
	38	"Design and Development of a High Frequency Switching Amplifier for Smart Material Actuators Using Improved Current Mode Control Strategy," J. Luan and F.C. Lee, VPEC Seminar, September 1997 and PESC '98.

- 37 "The Emitter Turn-Off Thyristor (ETO)," Y. Li and Alex Huang, VPEC Seminar, September 1997.
- 36 "Optimizing Low Voltage DC-DC Converters," X. Zhou, T. Wang, and F.C. Lee, VPEC Seminar, September 1997.
- 35 "A Single-Stage Electronic Ballast with Power Factor Correction and Low Crest Factor for Fluorescent Lamps," J. Qian, F.C. Lee, and T. Yamauchi, VPEC Seminar, September 1997.
- 34 "The Emitter-Controlled Thyristor (ECT)--A New MOS Gate Controlled Thyristor," Bo Zhang and Alex Q. Huang, VPEC Seminar, September 1997.
- 33 "Theoretical Limitation of the RBSOA of MOS-Controlled Thyristors," B. You and Alex Huang, VPEC Seminar, September 1997.
- 32 "A Unified View of MOS-Gated Thyristors," Alex Q. Huang, Tutorial, VPEC Seminar, September 1997.
- 31 "Review of Power Factor Correction Techniques," H. Mao, Fred C. Lee, Y. Jiang, and D. Boroyevic, IPEMC'97, November 3-6, 1997.
- 30 "Multivariable Analysis and Control of the Three-Phase Boost Rectifier," S. Chandrasekaran and D. Boroyevic, VPEC Seminar, September 1997.
- 29 "A New Continuous Input Current Charge Pump Power Factor Correction (CIC-CPPFC) Electronic Ballast," J. Qian, F. C. Lee, T. Yamauchi, Tutorial, VPEC Seminar, September 1997. Also published in IEEE Transactions on Power Electronics, Vol. 13, No. 3, May 1998.
- 28 "Improved Zero-Current Transition Converters for High Power Applications," Hengchun Mao, Fred C. Lee, Xunwei Zhou, Heping Dai, Mohummet Cosan, and Dushan Boroyevich, IEEE Transactions on Ind. Applications, September/October 1997, Vol. 33, No. 5.
- 27 "Non-Destructive Testing and Evaluation of Generation II Mos-Controlled Thyristors," Xinxiang Yan, Second International Power Electronics and Motion Control Conference, November 1997.
- 26 "Phase Offset Compensation Method for Measurement of Low-power-factor High-frequency Inductors," W. Chen, L. Ye, D. Y. Chen, and F.C. Lee, VPEC Seminar, September 1997 and APEC '98.
- 25 "Non-Intrinsic Differential Mode Noise Caused by Ground Current in an Off-Line Power Supply," D. Zhang, D. Chen, and F.C. Lee, VPEC Seminar, September 1997.
- 24 "Analysis and Design of Active Clamp Forward Converter by the Dynamic Performance of the Circuit Reset Mechanism," Q. Li, M. Jovanovic, and F.C. Lee, VPEC Seminar, September 1997.
- 23 "Modeling and Control Design of Parallel Three Phase PWM Boost Rectifiers," Z. Ye, D. Boroyevic and F.C. Lee, VPEC Seminar, September 1997.
- 22 "Packaging of Power Electronics Building Blocks Using Metal Posts Interconnected Parallel Plate Structure," S. Haque, K. Xing, G.Q. Lu, D. Nelson, D. Boroyevic and F.C. Lee, VPEC Seminar, September 1997.
- 21 "Extraction of Parasitics within Wire-Bond IGBT Modules," K. Xing, F.C. Lee, and D. Boroyevic, VPEC Seminar, September 1997 and APEC '98.
- 20 "Design and Experimental Results of Quasi-Single-Stage Isolated Three-Phase ZVZCS Buck PWM Rectifier," K. Wang, S. Chandrasekaran, F.C. Lee, D. Boroyevich and Y. Wang, VPEC Seminar, September 1997.
- 19 "A Family of Quasi-Single-Stage Isolated Three-Phase ZVZCS Buck PWM Rectifiers," K. Wang, F.C. Lee, and D. Boroyevich, VPEC Seminar, September 1997.
- 18 "Design Improvement of the ZVT IGBT Soft-Switching Inverter for Induction Motor Applications," B.M. Song, Y. Tang, and J. Lai, VPEC Seminar, September 1997.
- 17 "Drive Cycle Evaluation of a Soft-Switched Electric Vehicle Inverter," D. Katsis, M. Herwald, F.C. Lee, and D. Boroyevich, VPEC Seminar, September 1997.
- 16 "DSP-Based Implementation of a Power Electronic System," V.H. Prasad, S. Dubovsky, D. Boroyevic and N. Celanovic, VPEC Seminar, September 1997.

- 15 "Analysis and Design of a Three-Phase Inverter with a Neutral Leg," R. Zhang, V. Prasad, D. Boroyevich, and F.C. Lee, VPEC Seminar, September 1997.
- 14 "New High-Power, High-Performance Power Converter Systems," R. Zhang, F.C. Lee, D. Boroyevich, H. Mao, V. Prasad, N. Celanovic, X. Jing, VPEC Seminar, September 1997. Also presented at PESC '98.
- 13 "Design of a Power Factor Correction Circuit with Wide Output Voltage Range," Y. Zhao, C.Y. Lin, X. Zhuang and F.C. Lee, VPEC Seminar, September 1997.
- 12 "Single-Stage Single-Switch Power Factor Correction (S^4 -PFC) AC/DC Converters with DC Bus Voltage Feedback for Universal Line Applications," J. Qian, Q. Zhao, and F.C. Lee, VPEC Seminar, September 1997.
- 11 "Design of High Efficiency, Low Profile, Low Voltage Converter with Integrated Magnetics," W. Chen G. Hua, D. Sable, and F.C. Lee, VPEC Seminar, September 1997.
- 10 "Investigation of Candidate VRM Topology for Future Microprocessors," X. Zhou, VPEC Seminar, September 1997 and APEC '98.
- 9 "VRM Transient Study and Output Filter Design for Future Processors," P. Wong, H. Wu, J. Chen, X. Zhou, J. Liu, X. Zhang, L. Amoroso, VPEC Seminar, September 1997.
- 8 "A Three Phase Zero Voltage Transition Inverter with Inductor Feedback," S.C. Frame, D. Katsis, D. H. Lee, D. Borojevic, and F. C. Lee," Seventh European Conference on Power Electronics and Applications.
- 7 "Nonlinear Control of a Proof-Mass Actuator," D. K. Lindner, G. A. Zvonar, D. Borojevic, Journal of Guidance, Control and Dynamics, Vol. 20, No. 3, May-June 1997.
- 6 "Improved Winding Design for Planar Inductors," Liming Ye, Glenn R. Skutt, Ronald Wolf, and Fred C. Lee, PESC'97, VPEC Seminar, September 1997.
- 5 "Comparison of High-Frequency Application of Silicon Rectifiers" GaAs, Rectifier, and ZVT Technolog in a PFC Boost Converter," Xunwei Zhou, Mike Elmore and Fred C. Lee, PESC '97.
- 4 "A Simplified Model of a Variable Speed Synchronous Generator Loaded with Diode Rectifier," Ivan Jadric, Dusan Borojevic and Martin Jadric, PESC '97.
- 3 "Novel Zero-Voltage-Transition and Zero-Current-Transition Pulse-Width-Modulation Converters," Dong Ho Lee and Fred C. Lee, PESC '97. Also presented at VPEC Seminar, September 1997.
- 2 "Charge Pump High Power Factor Dimming Electronic Ballast," Jinrong Qian, Fred C. Lee, Tokushi Yamauchi, PESC '97. Also presented as a Tutorial at VPEC Seminar, September 1997.
- 1 "Single-Power-Supply-Based Transformerless IGBT/MOSFET Gate Driver with 100% High-Side Turn-on Duty Cycle Operation Performance Using Auxiliary Bootstrapped Charge Pumper," Ray L. Lin and Fred C. Lee, PESC '97

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- 56 "A Procedure for Designing EMI Filters for Ac Line Applications," Fu-Yuan Shih, Dan Y. Chen, Yan-Pei Wu, and Yie-Tone Chen, IEEE Transactions on Power Electronics, Vol. 11, No. 1, January, 1996.
- 55 "PEBB and System Integration at Virginia Power Electronic Center," F. C. Lee, D. Borojevic, K. Xing, G. Thandi, and H. Zhu, GOMAC '97.
- 54 "Evaluation and Development of New Power Electronic Technologies for Superconducting Magnetic Energy Storage (SMES) Using PEBB", H. Mao, D. H. Lee, H. Dai, F. C. Lee, D. Borojevic, GOMAC '97. Also presented at VPEC Seminar, September 1997.
- 53 "Current Source Charge Pump Power Factor Correction Electronic Ballast," Jinrong Qian, Fred C. Lee and T. Yamauchi, PESC '97.

- 52 “Design Procedure and Modeling of High Power, High Performance Zero-Voltage Zero-Current Switched, Full-Bridge PWM Converter,” C. Cuadros, C. Y. Lin, D. Boroyevich, R. Watson, G. Skutt, F. C. Lee, and P. Ribardiere, APEC '97.
- 51 “Analysis and Comparison of Space Vector Modulation Schemes for a Four-leg Voltage Source Inverter,” V. H. Prasad, D. Boroyevich, and Richard Zhang, APEC '97.
- 50 “Computer Modeling and Analysis of VSI Fed Permanent Magnet Synchronous Motor Drive Systems with Adjustable Levels of Complexity,” Zoran Mihailovic, V. Prasad, and D. Borojevic APEC '97.
- 49 “Design and Analysis of Thermal Management for High-Power-Density Converters in Sealed Enclosures,” Michael T. Zhang, Milan M. Jovanovic, and Fred C. Lee, APEC '97.
- 48 “Analysis and Design of N Paralleled DC-DC Converters with MS Current-Sharing Control,” Yuri Panov, Jay Rajagopalan, and Fred C. Lee, APEC '97.
- 47 “Conducted EMI Analysis of a Boost PFC Circuit,” Wei Zhang, Michael T. Zhang, Fred C. Lee, James Roudet and Edith Clavel, APEC '97.
- 46 “Optimizing Design for Low Voltage DC-DC Converter,” Xunwei Zhou, T. Wang, and Fred C. Lee, APEC '97.
- 45 “Zero-Voltage-Transition DC-Link Techniques for Three-Phase AC-DC-AC PWM Converters,” Hengchun Mao, J. Zhang, F. C. Lee and D. Borojevic, APEC '97.
- 44 “Analysis, Design and Experiments of a High Power Factor Electronic Ballast,” Jinrong Qian, Fred C. Lee, Tokushi Yamauchi, APEC '97.
- 43 “A High Efficient Single Stage Single Switch High Power Factor AC/DC Converter with Universal Input,” Jinrong Qian and Fred C. Lee, APEC '97.
- 42 “Design Considerations and Performance Evaluations of Synchronous Rectification in Flyback Converters,” Michael T. Zhang, Milan M. Jovanovic, and Fred C. Lee, APEC '97. Also published in IEEE Transactions on Power Electronics, Vol. 13, No. 3, May 1998.
- 41 “Single-Switch Soft-Switching Flyback Converter,” J.B. Lio, M.S. Lin, D. Y. Chen, and W. S. Feng, published in Electronics Letters, August 1, 1996, Vol. 32, No. 16.
- 40 “Primary-Side Dimming Control Driver for Cold-Cathode Fluorescent Lamps,” Mu-Shen Lin, Jan-Bin Lio, Dan Y. Chen and Wu-Shiung Feng, published in Electronics Letters, July 18, 1996, Vol. 32, No. 15.
- 39 “Synchronous Dimming Control for a Cold-Cathode Fluorescent Lamp Driver,” M.S. Lin, M.C. Lee, D. Y. Chen, and W.S. Feng, published in Electronics Letters, June 20, 1996, Vol. 32, No. 13.
- 38 “A Single Switch Buck Converter for Hybrid Electric Vehicle Generators,” D. Katsis and F. C. Lee, IEEE Workshop on Power Electronics in Transportation, October 24- 25, 1996.
- 37 “Stability of Large DC Power Systems Using Switching Converters, with Application to the International Space Station,” E.W. Gholdston, K. Karimi, F.C. Lee, J. Rajagopalan, Y. Panov, B. Manners, 31st Intersociety Energy Conversion Engineering Conference, August 11-16, 1996.
- 36 “A Three-Phase Inverter with a Neutral Leg with Space Vector Modulation,” Richard Zhang, Dushan Boroyevich, V. H. Prasad, Hengchun Mao, Fred C. Lee and Stephen Dubovsky, VPEC Seminar, September 1996 and APEC '97.
- 35 “SVM of Three-Level VSI's,” M. Cosan, H. Mao, D. Boroyevich, and F. C. Lee, VPEC Seminar, September 1996.
- 34 “Comparison of High Frequency PWM Algorithms for Voltage Source Inverter,” V. Prasad, D. Borojevic, S. Dubovsky, and S. Chandrasekaran, VPEC Seminar, September 1996.
- 33 “Design-Oriented Analysis of Paralleled DC-DC Converters with Master-Slave Current-Sharing Control,” Y. Panov, J. Rajagopalan, and F. C. Lee, VPEC Seminar, September 1996.
- 32 “Characterization and Analysis of Electromagnetic Interference in a High Frequency AC Distributed Power System,” Michael Zhang, Robert Watson, Fred C. Lee, James Roudet, Jean-Luc Shanen, and Edith Clavel, VPEC Seminar, September 1996.

- 31 "Design of a High Power, High Performance ZVZCS-FB-PWM Converter," C. Cuadros and Dushan Boroyevich, VPEC Seminar, September 1996.
- 30 "A New Method to Characterize EMI Filters," Dongbing Zhang, Dan Y. Chen, and Dan Sable, VPEC Seminar, September 1996 and APEC '98.
- 29 "High-Voltage Applications of Piezoelectric Transformers," C.Y. Lin and Fred C. Lee, VPEC Seminar, September 1996.
- 28 "Measurement Issues in the Characterization of Ferrite Magnetic Material," Glenn Skutt, Fred C. Lee, and John G. Breslin, VPEC Seminar, September 1996.
- 27 "Optimum PWM Pattern for a Three-Phase Boost DCM PFC Rectifier," Richard Zhang and Fred C. Lee, VPEC Seminar, September 1996 and APEC '97.
- 26 "Characterization and Control of Three-Phase Boost Rectifiers at Light Load," Qihong Huang and Fred C. Lee, VPEC Seminar, September 1996.
- 25 "A Generalized Technique for Derivation of Linear Average Current Mode Control Laws for Power Factor Correction without Input Voltage Sensing," Jay Rajagopalan and Fred C. Lee, VPEC Seminar, September 1996 and APEC '97.
- 24 "Investigation of Charge-Pump-Controlled High Power Factor Correction AC-DC Converters," Jinrong Qian and Fred C. Lee, VPEC Seminar, September 1996.
- 23 "A Novel High Power Factor Correction Rectifier with a Coupling Inductor," Jinrong Qian and Fred C. Lee, VPEC Seminar, September 1996.
- 22 "Zero Voltage and Zero Current Switching Full Bridge PWM Converter Using Secondary Active Clamp," Jung G. Cho, Geun H. Rim, and Fred C. Lee, PESC '96
- 21 "Improved Zero-Current Transition Converters for High Power Applications," H. Mao, Fred C. Lee, Xunwei Zhou, and Dushan Boroyevich, IAS '96. Also published in IEEE Transactions on Ind. Applications, September/October, 1997.
- 20 "High-Frequency Switching Amplifiers for Electrostrictive Actuators," G. A. Zvonar, J. Luan, F. C. Lee, 1996 SPIE Symposium on Smart Structures and Materials, San Diego, CA., February 26-29, 1996.
- 19 "Harmonic Reduction in a Single-Switch, Three-Phase Boost Rectifier with High Order Harmonic Inject PWM," Qihong Huang, Fred C. Lee, PESC '96.
- 18 "A New Quasi-Single-Stage Isolated Three-Phase ZVZCS Buck PWM Rectifier," K. Wang, Fred C. Lee, Xinxiang Yan and Dushan Boroyevich, PESC '96.
- 17 "An Experimental Comparison of Conducted EMI Emissions Between Zero-Voltage Transition Circuit and a Hard-Switching Circuit," Dongbing Zhang, Dan Y. Chen and Fred C. Lee, PESC '96.
- 16 "Small-Signal Modeling of Three-Phase PWM Modulator," S. Hiti, D. Boroyevich, PESC '96.
- 15 "Novel Reduced-Order Small-Signal Model of Three-Phase PWM Rectifiers," Hengchun Mao, Dushan Boroyevich and Fred C. Lee, PESC '96. Also published in IEEE Transactions on Power Electronics, No. 3, Vol. 13, May 1998.
- 14 "Novel Soft-Switched Three-Phase PWM Converters with Reduced Auxiliary Switch Stresses," H. Mao, Fred C. Lee, PESC '96, VPEC '96
- 13 "Novel-Zero-Current-Switching-Zero-Voltage-Switching Converters," Ray L. Lin and Fred C. Lee, PESC '96.
- 12 "A Novel ZVT Three-Phase Rectifier/Inverter with Reduced Auxiliary Switch Stresses and Losses," Q. Li, Zunwei Zhou and Fred C. Lee, PESC '96.
- 11 "Characterization and Analysis of Electromagnetic Interference in a High Frequency AC Distributed Power System," M.T. Zhang, R. Watson and Fred C. Lee, PESC '96.

- 10 "Characterization of Dimensional Resonance Effects in Ferrite-Core Magnetic Devices," G. Skutt and Fred C. Lee, PESC '96.
- 9 "Characterization and Analysis of Parasitic Parameters and their Effects in Power Electronics Circuit," Ning Dai and Fred C. Lee, PESC '96.
- 8 "Commutation Analysis of Self-Driven Synchronous Rectifiers in an Active-Clamp Forward Converter," Michael T. Zhang and Fred C. Lee, PESC '96.
- 7 "Reduction of Voltage Stress in Charge Pump Electronic Ballast," Wei Chen and Fred C. Lee, PESC '96.
- 6 "An Improved 'Charge Pump' Electronic Ballast with Low THD and Low Crest Factor," Wei Chen and Fred C. Lee, APEC '96. Also published in IEEE Transactions on Power Electronics, September 1997, Volume 12, No. 5.
- 5 "Design of a High Density Low-Profile Transformer," Ning Dai and Fred C. Lee, APEC '96
- 4 "Analysis and Design of High Frequency Three-Phase Boost Rectifiers," Hengchun Mao, Dushan Boroyevich, Ambatiputi Ravindra and Fred C. Lee, APEC '96
- 3 "Multi-Level 2 Quadrant Boost Choppers for Superconducting Magnetic Energy Storage," Hengchun Mao, Dushan Boroyevich and Fred C. Lee, APEC '96.
- 2 "Modeling and Stability Analysis of a DC Power System with Solid State Power Controllers," Y.V. Panov and F.C. Lee, APEC '96
- 1 "Modeling and Dynamic Analysis of Paralleled dc/dc Converters with Master-Slave Current Sharing Control," J. Rajagopalan, K. Xing, Y. Gui, and F.C. Lee, APEC '96.

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- 61 "High Performance Three-Phase Power Factor Correction Circuits," Hengchun Mao, Fred C. Lee, Dushan Boroyevich, and Silva Hiti, IECON '95. Also published in IEEE Transactions on Ind. Electronics, August 1997, Vol. 44, No. 4.
- 60 "Analysis of Trench Double Gate IGBT," Qin Huang and Gehan Amaratunga, Journal of Solid State Electronics, Vol. 38, No. 4, pp. 829-839, 1995.
- 59 "Forward Blocking Capability of Double Gate IGBTs at High Temperatures," Qin Huang and Gehan Amaratunga, Vol. 38, No. 5, 981-982, Journal of Solid State Electronics, 1995.
- 58 "MOS Controlled Diodes-A New Class of Fast Switching, Low Loss Power Diodes," Qin Huang and Gehan Amaratunga, Vol. 38, No. 5, 977-980, Journal of Solid State Electronics, 1995.
- 57 "Analysis of 4.5KV Double Trench Gate MOS Controller Thyristor," Alex Q. Huang, Gehan Amaratunga and Dan Chen, ISPSD '95, pp. 153-158, Japan, 1995.
- 56 "IGPT - A New MOS Controller Power Transistor," Alex Q. Huang, published in IEEE Electron Device Letters, Vol. 16, No. 9, pp. 408, 1995.
- 55 "Effect of LIGBT Operation on CMOS Latch Up", Alex Q. Huang, published in IEEE Transactions on Electron Device, Vol. 42, No. 10; pp. 1873, 1995.
- 53 "Intermediate Line Filter Design to Meet Both Impedence Compatibility and EMI Specifications," B. Choi and B.H. Cho, published in IEEE Transactions on Power Electronics, Vol. 10, No. 5, pp. 583-588, September 1995.
- 52 "Evaluation of Synchronous-Rectifier Efficiency Improvement Limits in Forward Converters," M.M. Jovanovic, M.T. Zhang, and F.C. Lee, published in IEEE Transactions on Industrial Electronics, Vol. 42, No. 4, pp. 387-395.
- 51 "Recent Developments of Power Semiconductor Devices," A.Q. Huang; VPEC Seminar, September 1995.
- 50 "Comparative Study of Three-Phase Soft-Switching Inverters for Electric Vehicle Applications," S. Frame, S. Dubovsky, Q. Li, C. Cuadros, D. Borojevic, and F.C. Lee; VPEC Seminar, September 1995.

- 49 "Analysis, Design and Experimental Results of a Three-Phase ZVT Boost Rectifier," R. Zhang and F.C. Lee; VPEC Seminar, September 1995.
- 48 "Average Current Control of Three-Phase PWM Boost Rectifiers," S. Hiti, D. Borojevic, R. Ambatipudi, and R. Zhang; VPEC Seminar, September 1995.
- 47 "Design of a High-Efficiency, Low-Profile Forward Converter with 3.3 V Output," W. Chen, N. Dai, G. Hua, D. Sable, and F.C. Lee; VPEC Seminar, September 1995.
- 46 "Design of a 5-kW, 48 V Output ZVZCS-PWM Converter," C.Y. Lin and F.C. Lee; VPEC Seminar, September 1995.
- 45 "An Improvement of a Non-Dimming Electronic Ballast for the Fluorescent Lamp," W. Chen and F.C. Lee; VPEC Seminar, September 1995.
- 44 "Piezoelectric Transformers and Their Applications," C.Y. Lin and F.C. Lee; VPEC Seminar, September 1995.
- 43 "Design of High Power Density Low-Profile Transformer," N. Dai and F.C. Lee; VPEC Seminar, September 1995.
- 42 "Prediction of the Power Losses of PWM Inverter Drives for Permanent Magnet Brushless Motors," J Yang and D.Y. Chen; VPEC Seminar, September 1995.
- 41 "Analysis of a MCT Based Three-Phase PWM Inverter," X. Zhu, Q. Li, M. Cosan, X. Zhang, F.C. Lee, D. Borojevic, D. Sable and G. Hua, VPEC Seminar, September 1995.
- 40 "Component Development for a High-Frequency AC-Distributed Power System," R. Watson, W. Chen, G. Hua, and F.C. Lee, VPEC Seminar, September 1995.
- 39 "A Comparative Study of Switching Loss of MCTs and IGBTs," X. Zhou, F.C. Lee, D. Borojevic, K. Wang, G. Hua and D. Sable, VPEC Seminar, September 1995.
- 38 "A Three-Phase ZVT Boost Rectifier with Improved Analog Controller," R. Zhang, F.C. Lee, and D. Borojevic, VPEC Seminar, September 1995 and PESC '96.
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- 36 "Harmonic Reduction In a Single-Switch, Three-Phase Boost Rectifier with High Order Harmonic Injected PWM," Q. Huang and F.C. Lee, VPEC Seminar, September 1995 and PESC '96.
- 35 "Development of Power Electronics for a Flywheel Energy Storage System," J. Zhang, X. Zhang, D. Borojevic, F.C. Lee, G.C. Hua, D. Sable, VPEC Seminar, September 1995.
- 34 "Analysis of a MCT Based Three-Phase PWM Inverters," X. Zhou, Q. Li, M. Cosa, X. Zhuang, F.C. Lee, D. Borojevic, D. Sable and G. Hua, VPEC Seminar, September 1995.
- 33 "Examining Flux Distributions in Ferrite Magnetic Structures," G. Skutt and F.C. Lee, SMM12 '95, Krakow, Poland.
- 32 "Small-Signal Characterization of Active-Clamp PWM Converters," G. Stojcic, F.C. Lee and S. Hiti, VPEC Seminar, September 1995.
- 31 "A Soft-Switched, Full-Bridge Boost Converter Employing an Active -Clamp Circuit," R. Watson and F.C. Lee, VPEC Seminar, September 1995.
- 30 "Stability of a DC Power System with Solid State Power Controllers," Y.V. Panov and F.C. Lee, VPEC Seminar, September 1995.
- 29 "Control System Design and Small-Signal Analysis of a Phase-Shift-Controlled Series-Resonant Inverter for Induction Heating," L. Grajales and F.C. Lee, VPEC Seminar, September 1995.
- 28 "Modeling and Analysis of Input Characteristics of Three-Phase Inverter-Fed Permanent Magnet Synchronous Motor Drive," M. Shrivathsan, F.C. Lee and B.H. Cho, VPEC Seminar, September 1995.
- 27 "Analysis and Design of High Frequency Three-Phase Boost Rectifier," M. Mao, D. Borojevic, A. Ravindra and F.C. Lee, VPEC Seminar, September 1995.

- 26 "Improvements on Zero-Voltage-Transition Three-Phase Rectifier/Inverter," H. Mao and F.C. Lee, VPEC Seminar, September 1995.
- 25 "An Experimental Comparison of Conducted EMI Emissions Between A Zero-Voltage Transition Circuit and a Hard Switching Circuit," D. Zhang, D.Y. Chen, and F.C. Lee, VPEC Seminar, September 1995 and PESC '96.
- 24 "Novel DC-Rail Soft-Switched Three-Phase Voltage-Source Inverters," K. Wang, Y. Jiang, S. Dubovsky, G.C. Hua, D. Borojevic, and F.C. Lee. VPEC Seminar, September 1995 and IEEE Transactions on Industry Applications, Vol. 33, No. 2, March/April 1997.
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- 22 "Design of a High Density Low-Profile Transformer," N. Dai and F.C. Lee, VPEC Seminar, September 1995.
- 21 "Design and Analysis of a Regulated Peak-Power Tracking System," P.T. Huynh and B.H. Cho, IECEC '95. Also published in IEEE Transactions on Aerospace and Electronic Systems, Vol. 32, No. 1, January 1996.
- 20 "Large-Signal Stability Analysis of PWM Converters," P.T. Huynh and B.H. Cho, IECEC '95.
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